

FIG. 1

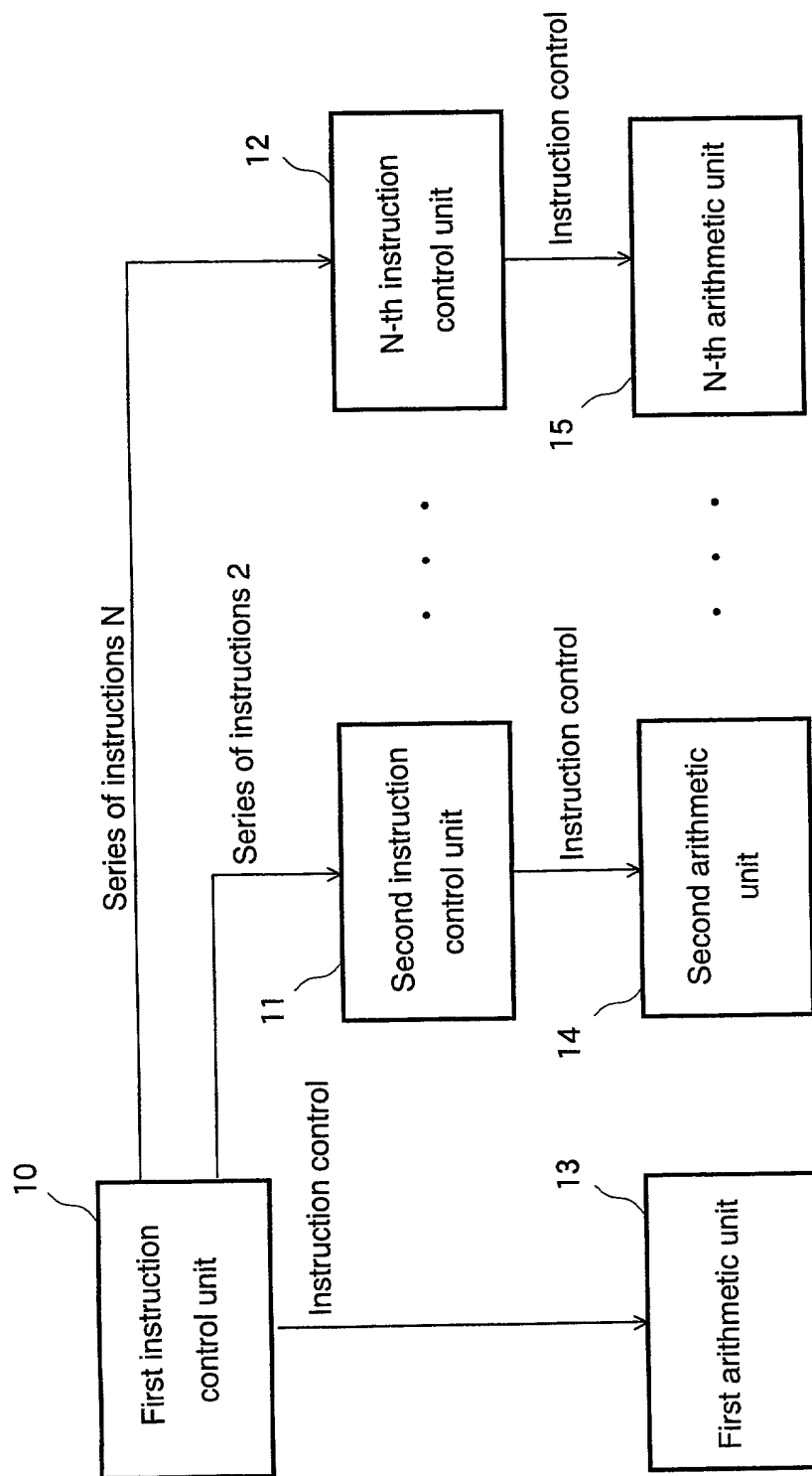


FIG. 2

Second instruction memory

First instruction memory

Address	Data
0000	Series of instructions 1
0001	Series of instructions 2
0002	Series of instructions 1
0003	Series of instructions 2 (Independent execution switching)
0004	Series of instructions 1
0005	Series of instructions 1
0006	Series of instructions 1
0007	Series of instructions 1
0008	Series of instructions 2

Address	Data
0000	Series of instructions 2+
0001	Series of instructions 2+
0002	Series of instructions 2+ (Synchronous execution switching)

FIG. 3

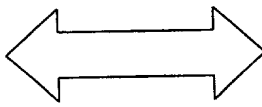
<div> <div>Fine granularity</div> <div>  </div> <div>Coarse granularity</div> </div>	Instruction level	<ul style="list-style-type: none"> • ADD A,B,C : Addition • MUL A,B,C : Multiplication
	Function level	<ul style="list-style-type: none"> • $\text{fft}(x,y)$: Fourier transform • $\text{fir}(x,y)$: FIR filter
	Application level	<ul style="list-style-type: none"> • MPEG Video (Image CODEC) • GSM-AMR(Voice CODEC)
	System level	<ul style="list-style-type: none"> • Portable terminal system

FIG. 4

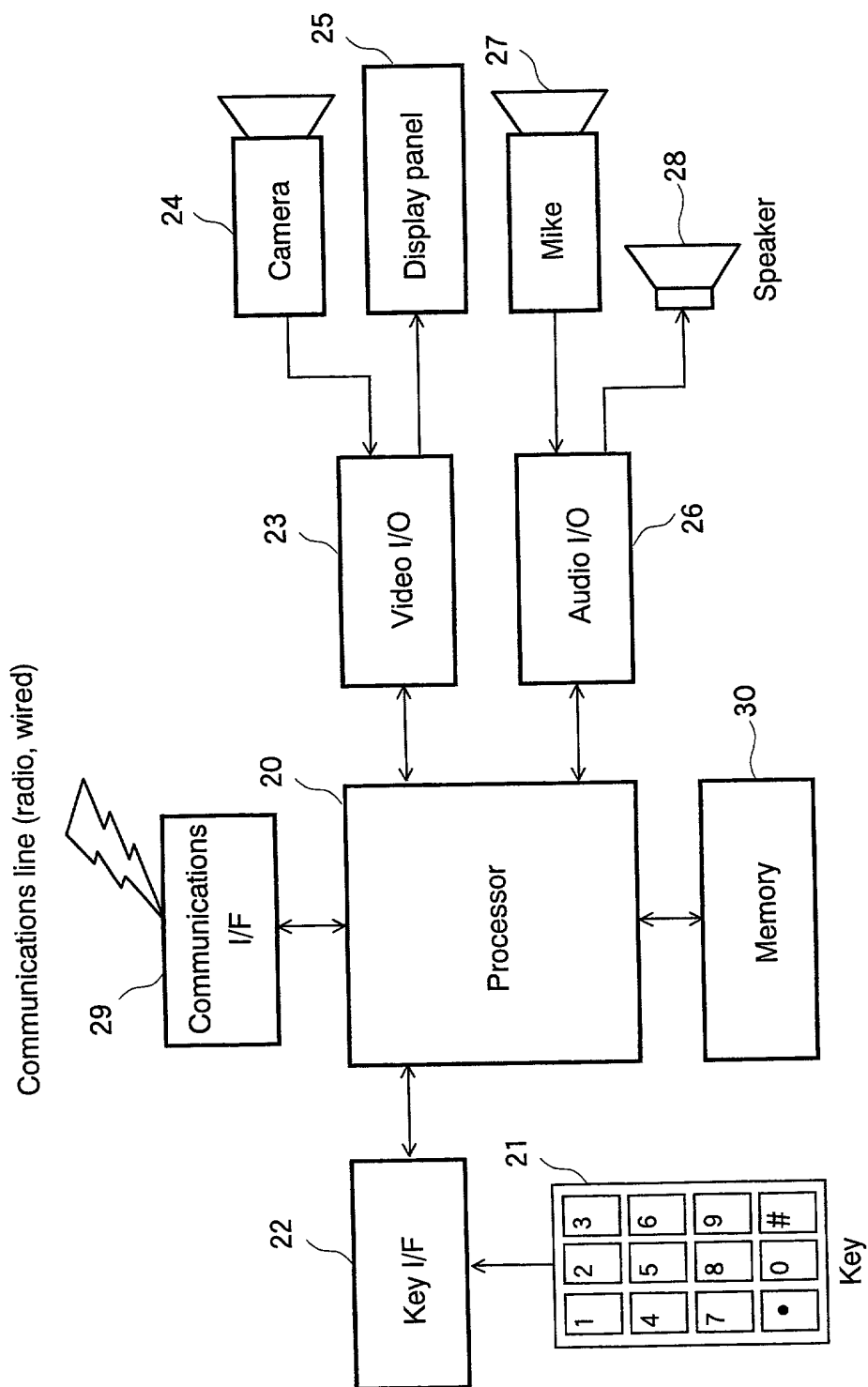


FIG. 5

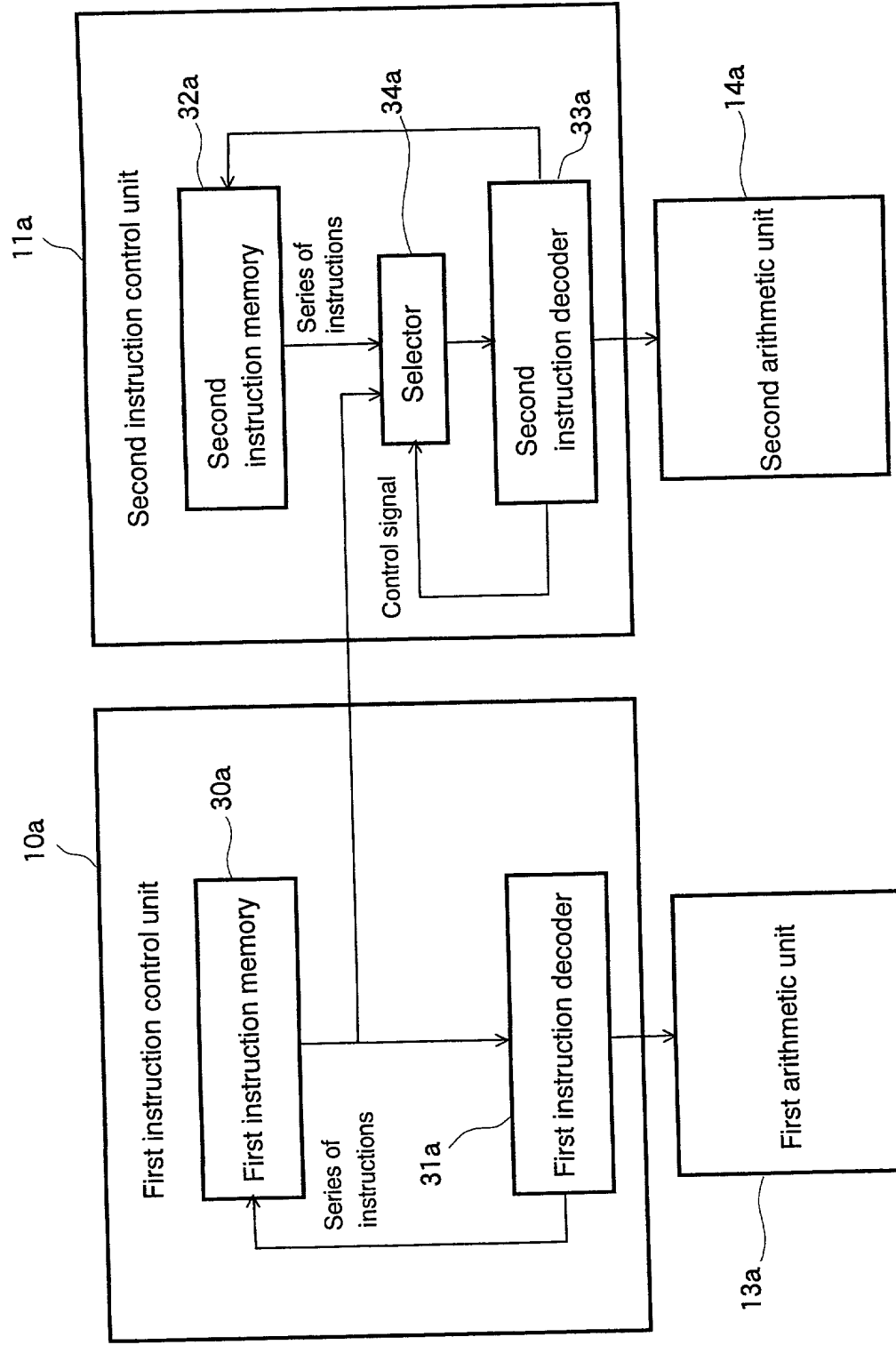


FIG. 7

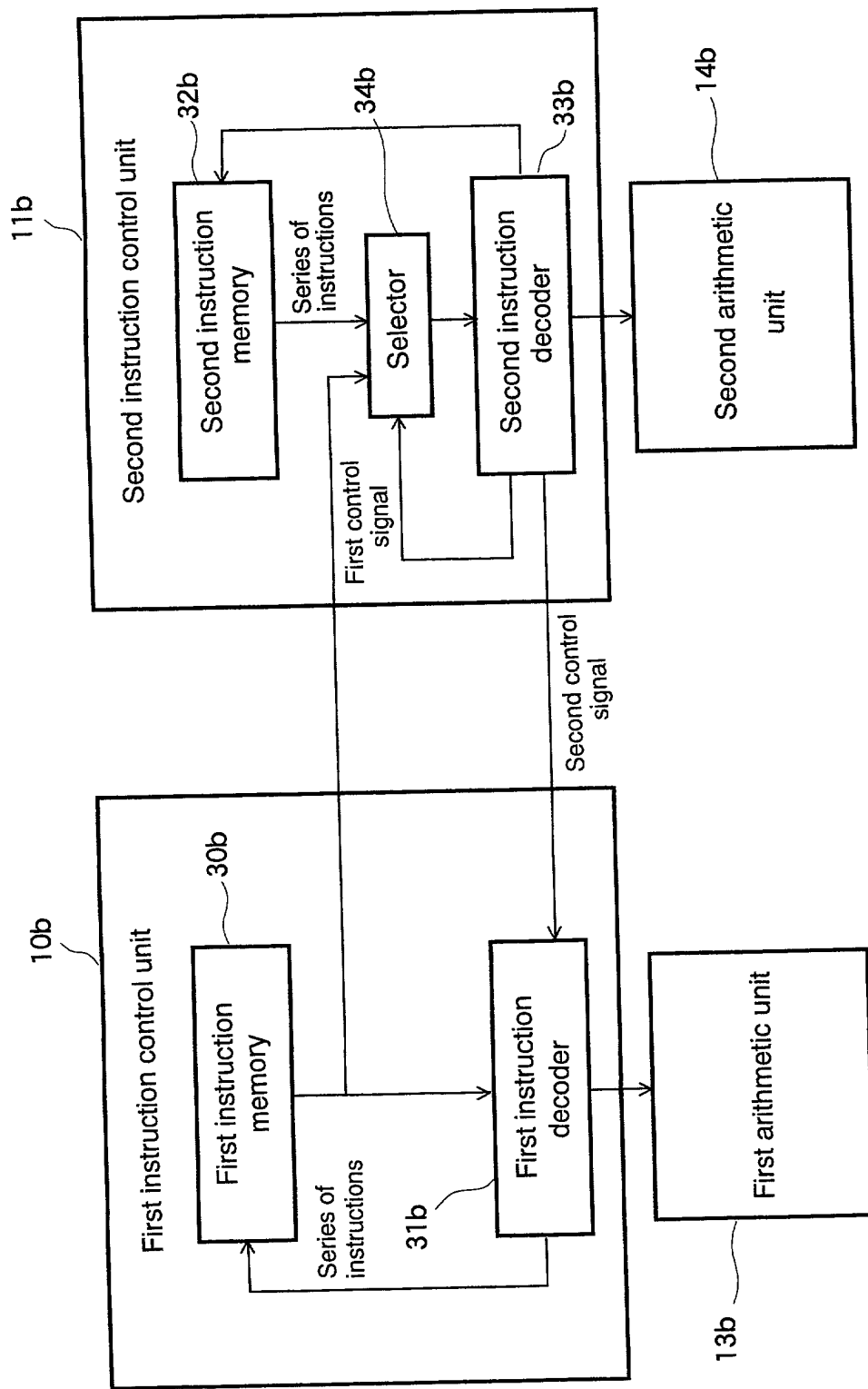


FIG. 8

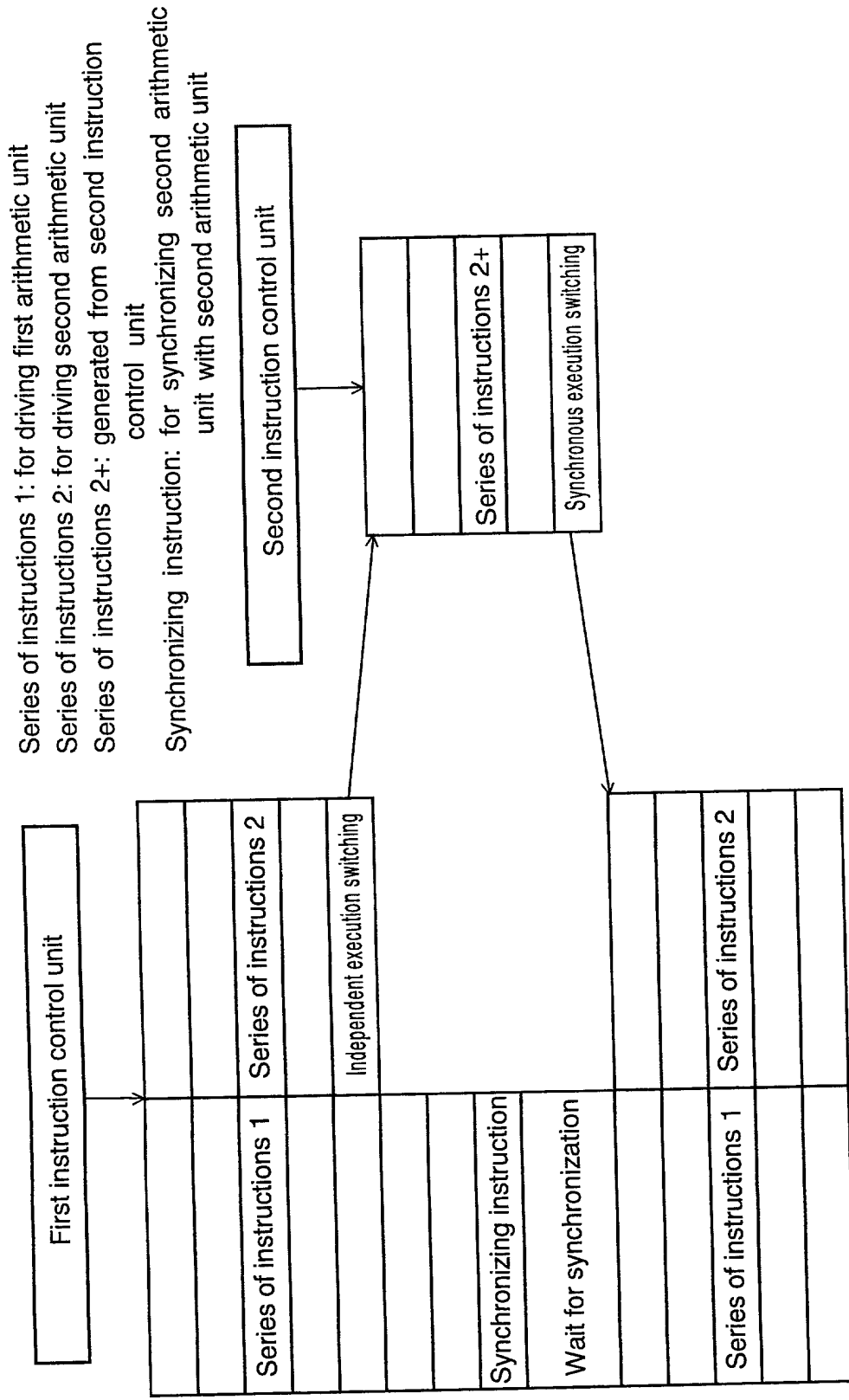


FIG. 9

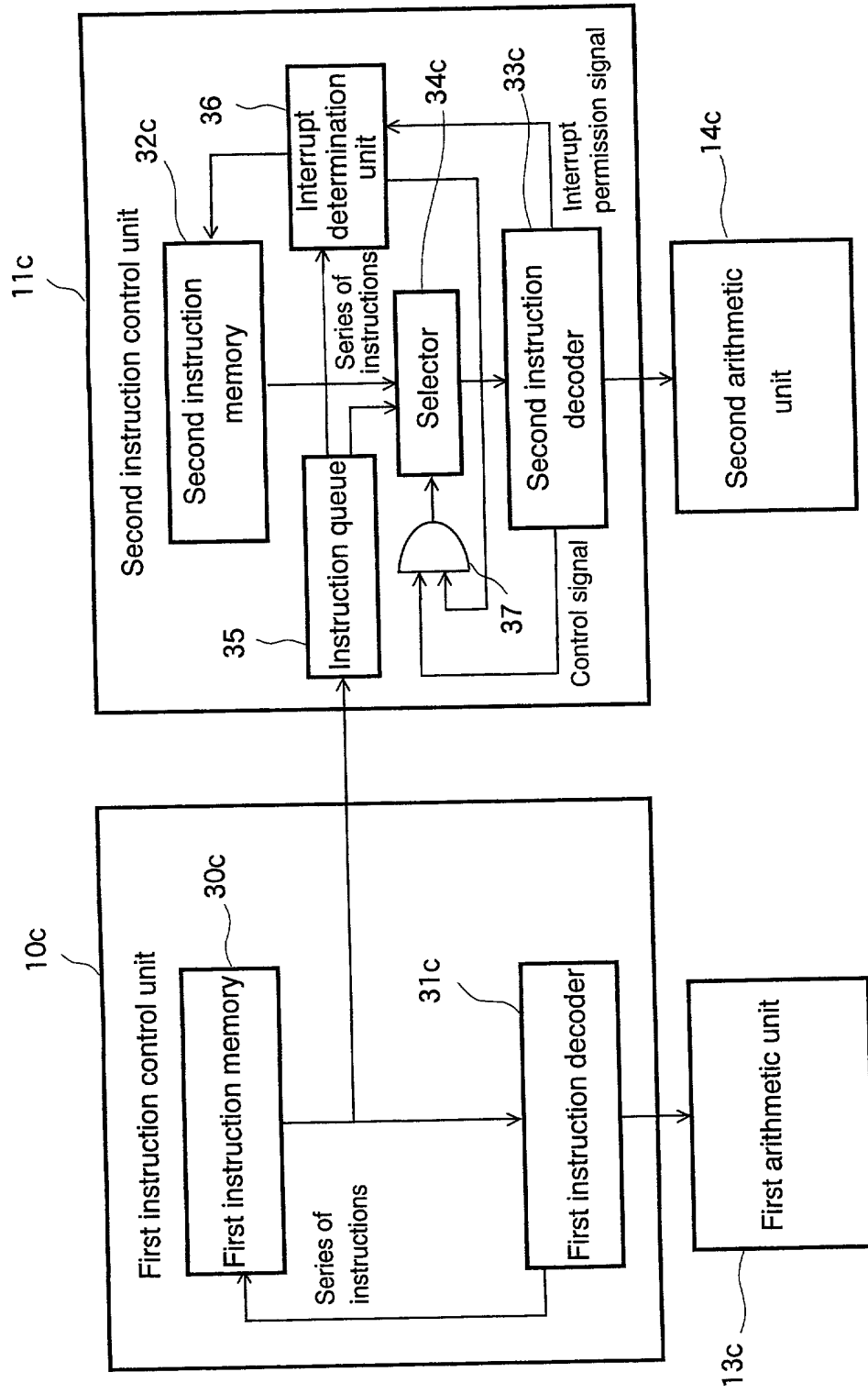


FIG. 10

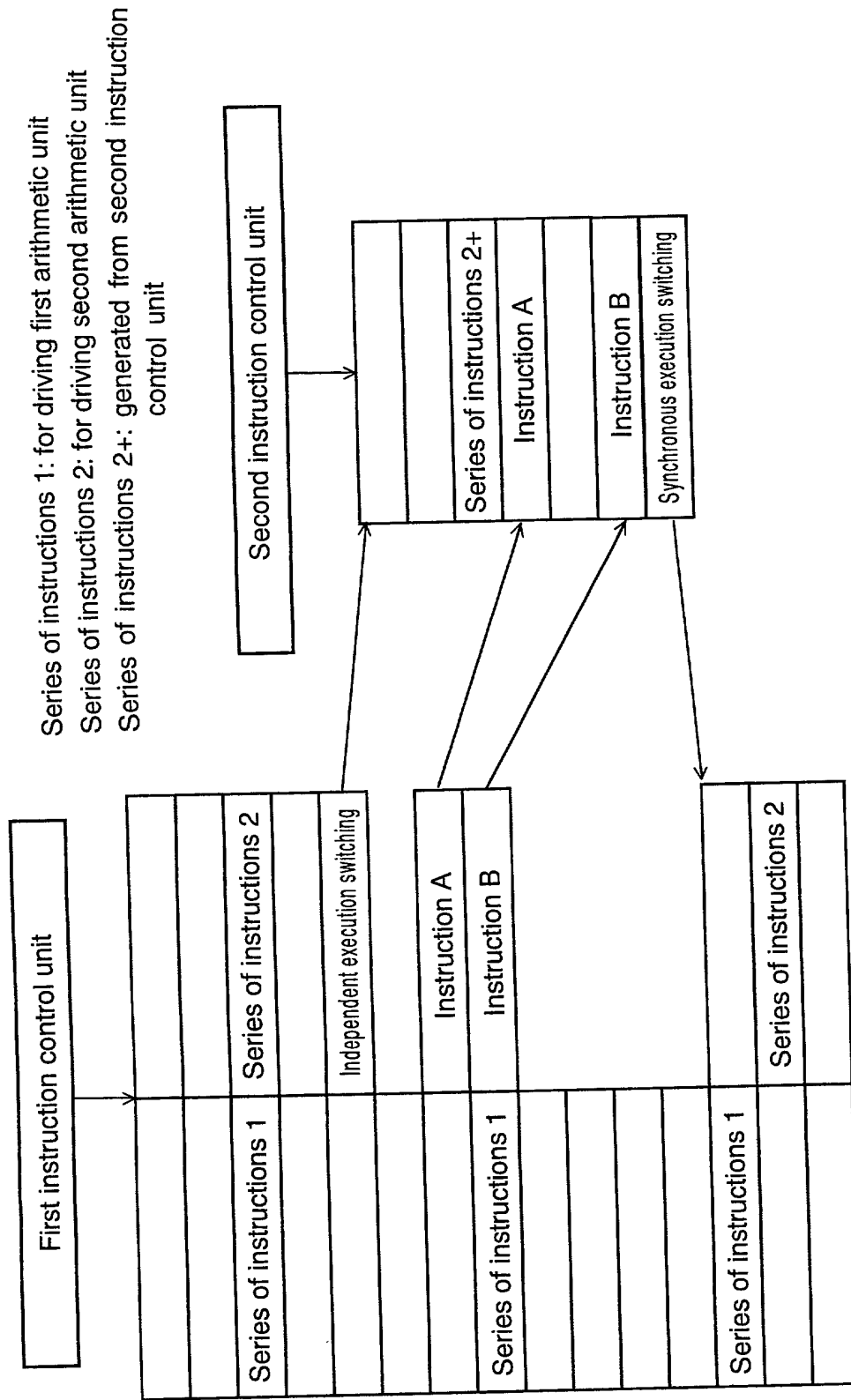


FIG. 11

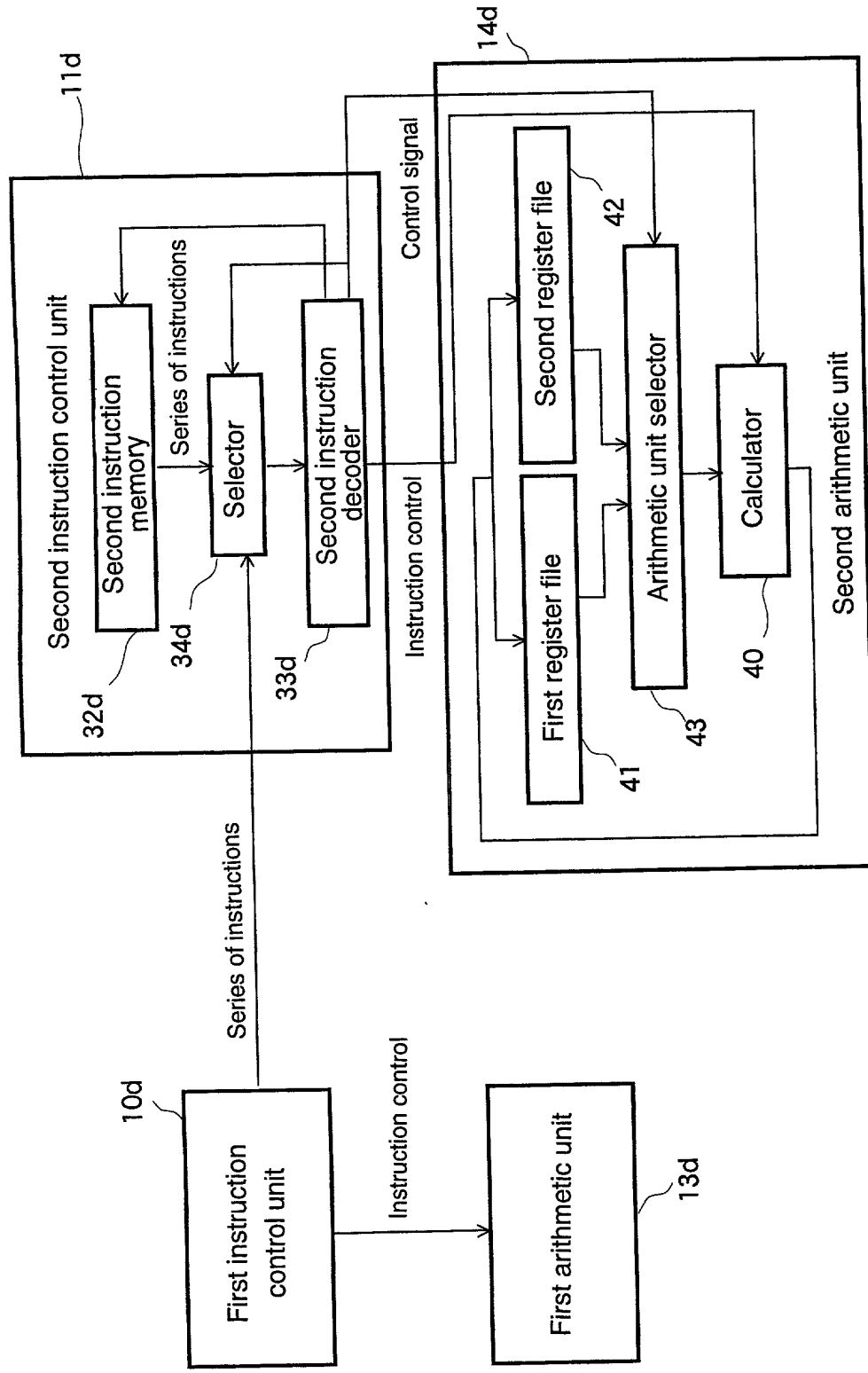


FIG. 12

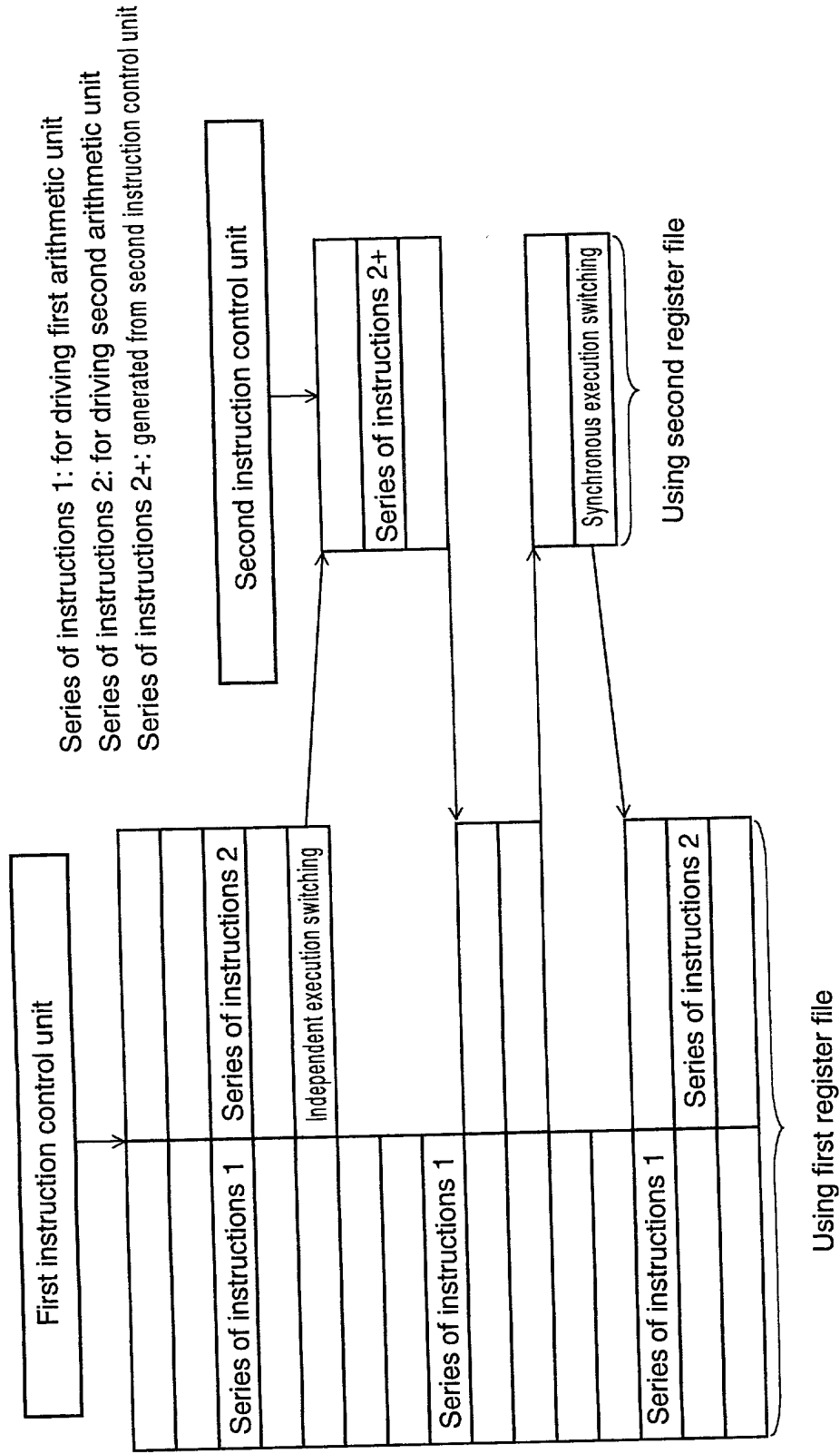


FIG. 13

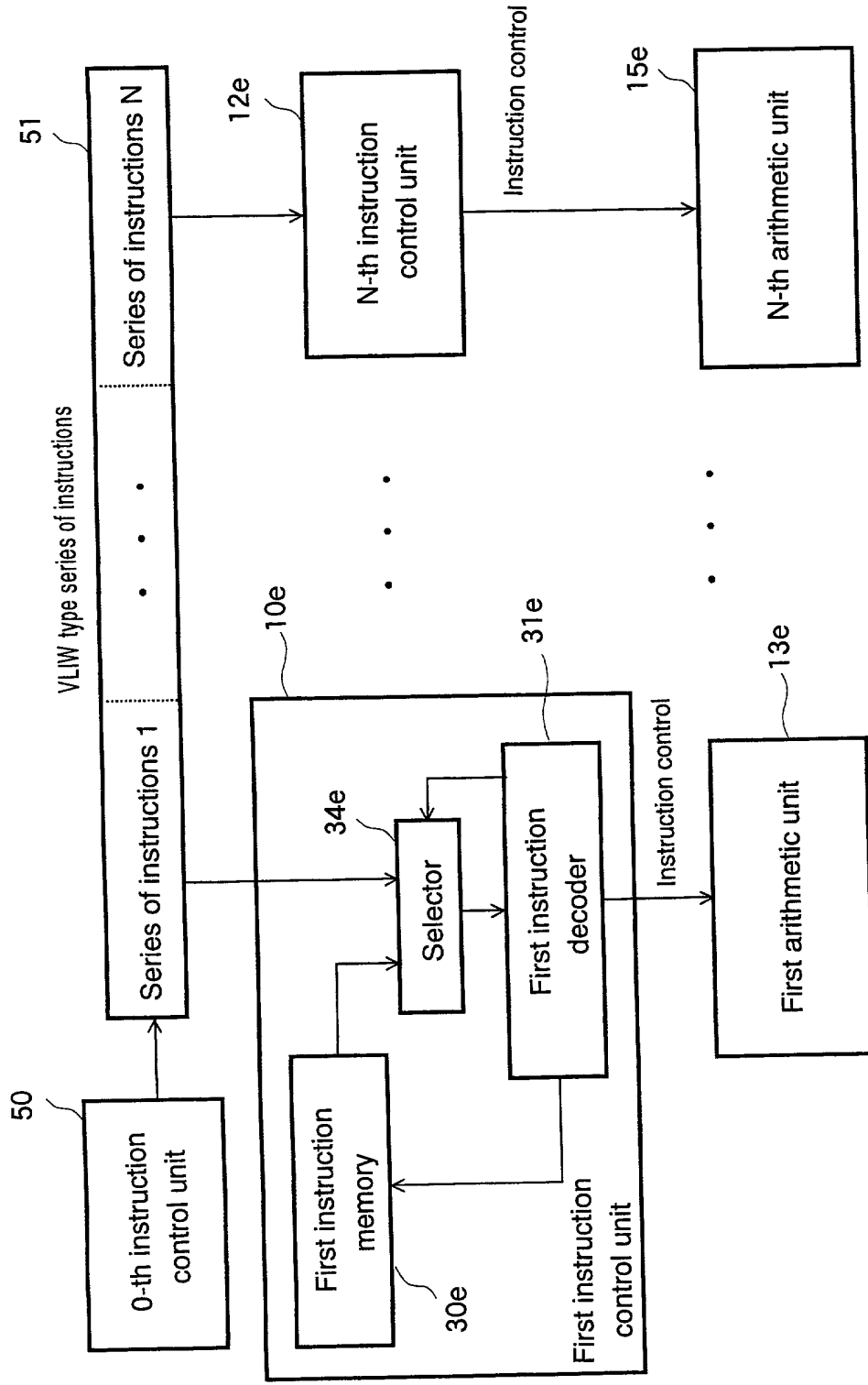


FIG. 14

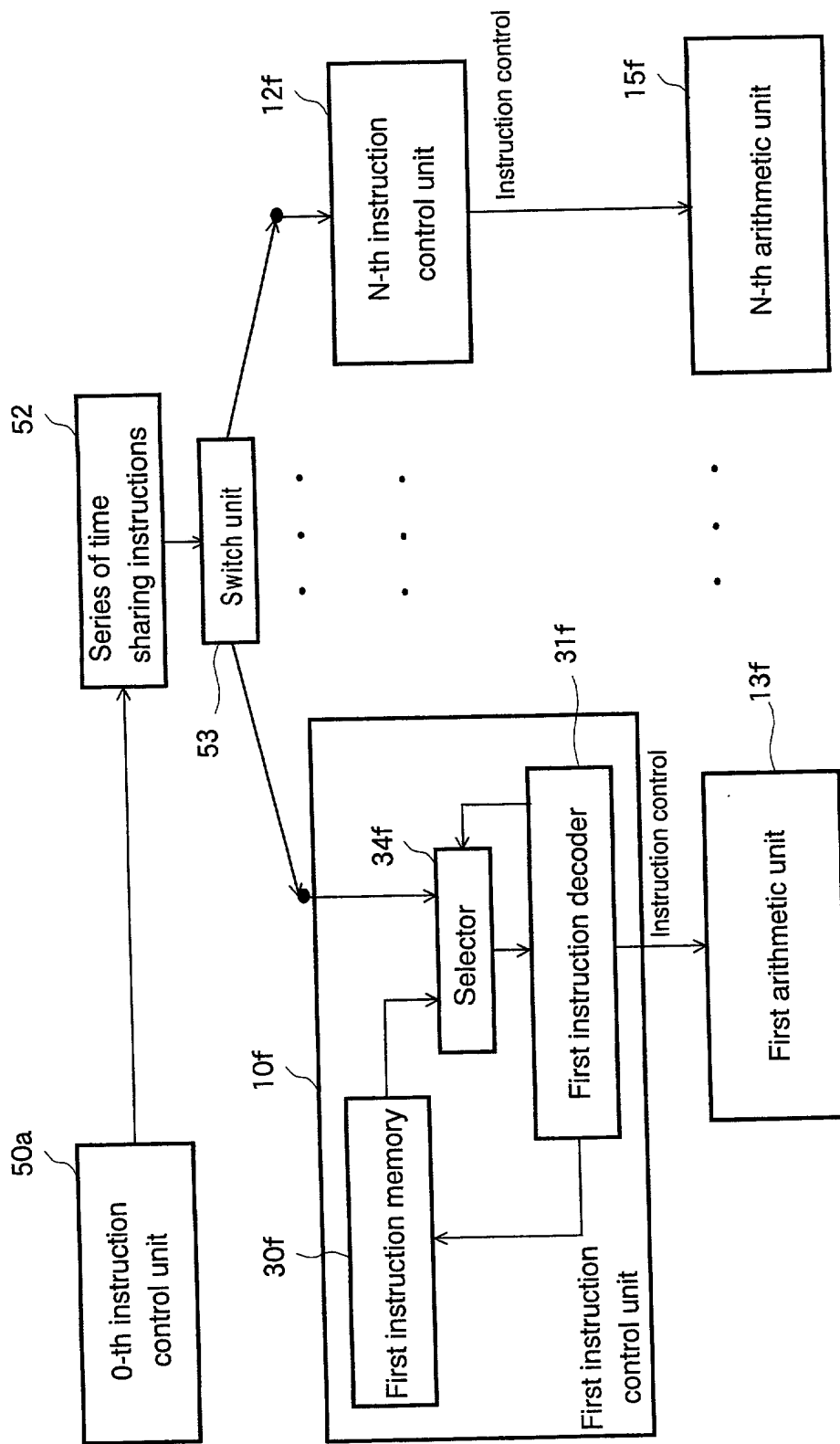


FIG. 16

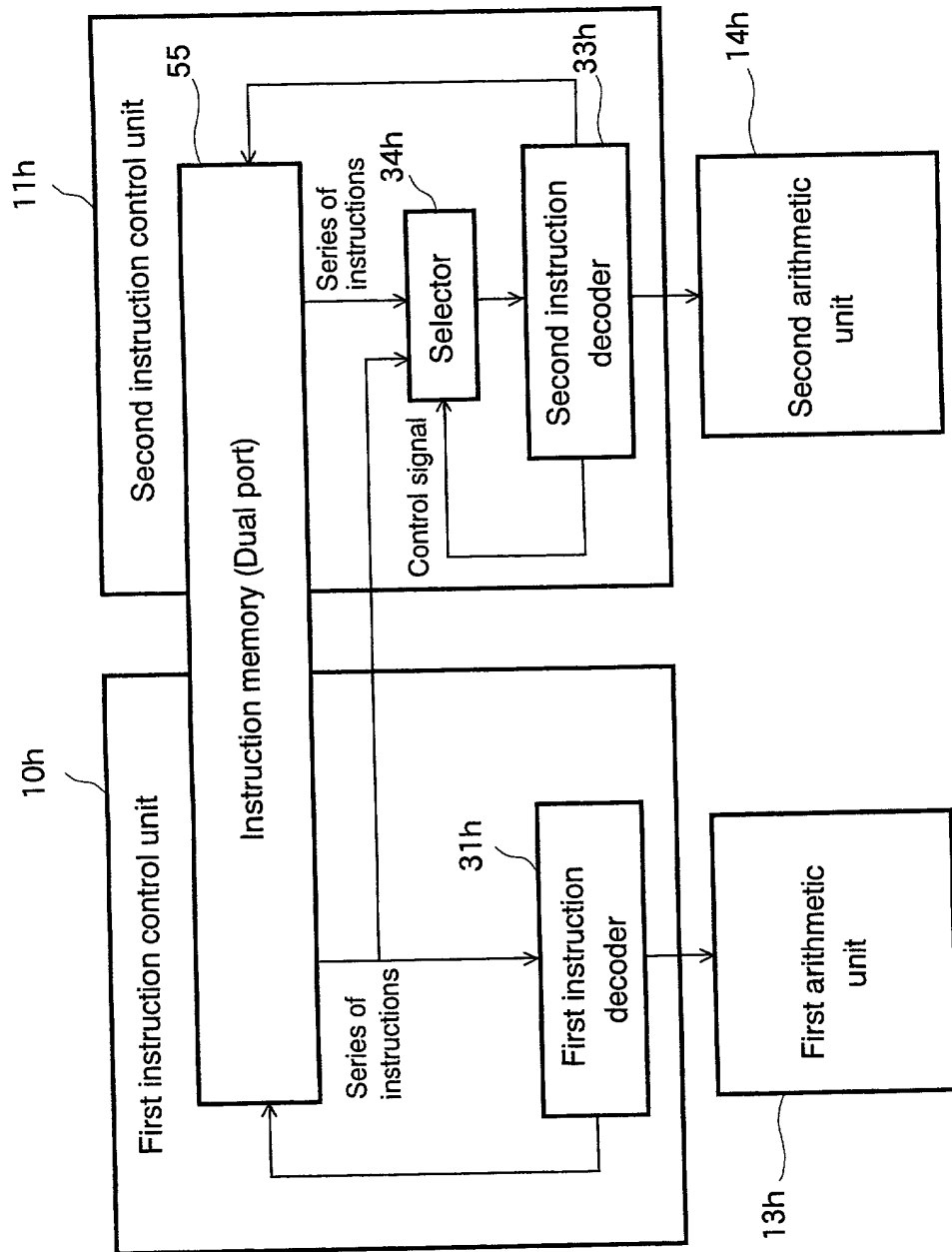


FIG. 17

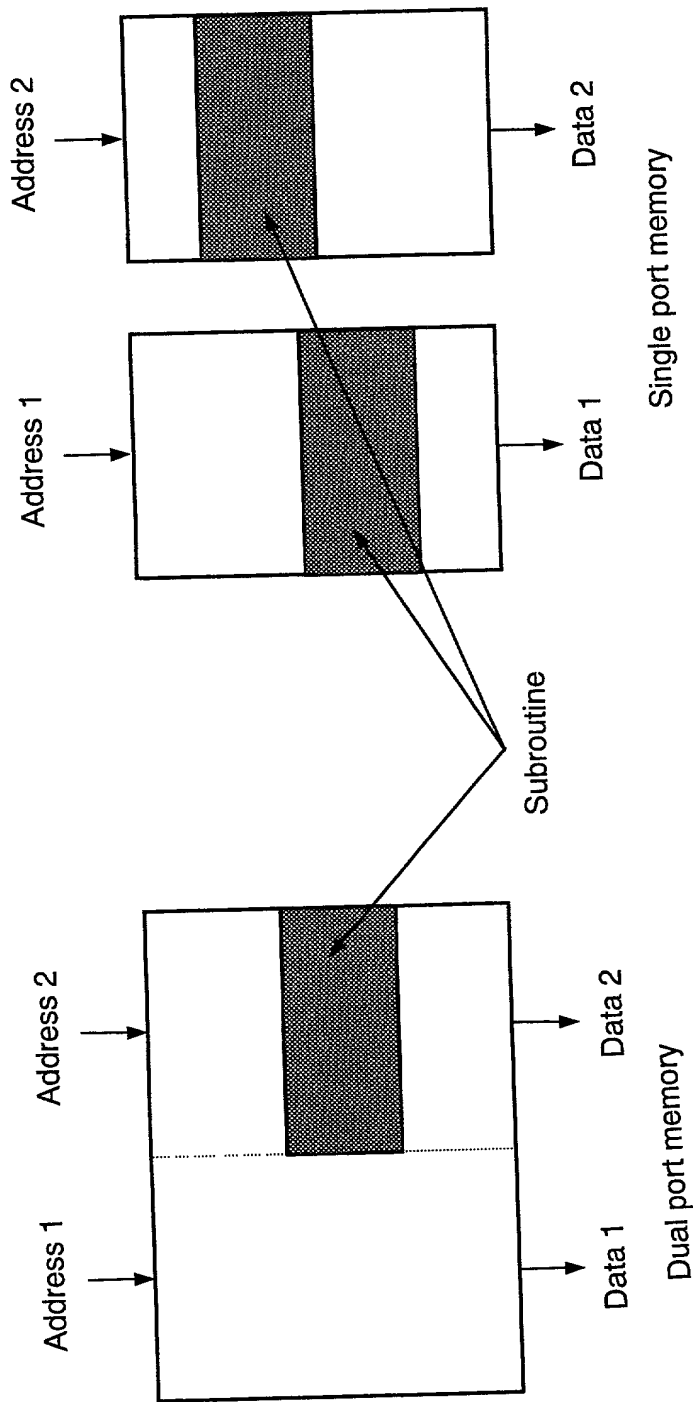


FIG. 18

